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Applicant: James E. Jaussi et al.

Filing Date: November 26, 2001

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OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

| **Examiner Initial | |
|-----------------------|--|
| W | Baker, R., et al., "CMOS: Circuit Design, Layout, and Simulation," <u>IEEE Press</u> , 4 pages, (1998) |
| W | Filanovsky, I.M., "Voltage Reference Using Mutual Compensation of Mobility and Threshold Voltage Temperature Effects", <u>ISCAS 2000- IEEE International Symposium on Circuits and Systems</u> , pp. V197-V200, (May 2000) |
| W | Haycock, M., et al., "A 2.5Gb/s Bidirectional Signaling Technology", <u>Hot Interconnects Symposium V</u> , pp. 1-8, (Aug. 1997) |
| W | Lee, S.H., et al., "A Temperature and Supply Voltage Insensitive CMOS Current Reference", <u>IEICE Trans. Electron.</u> , Vol. E82-C, No. 8, 1562-1566, (August, 1999.) |

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